

Claims

- [c1] What is claimed is:
1. A differential signal transmitter comprising:
a driver circuit that generates a differential signal in response to inputted data,
a voltage amplitude of the differential signal being controlled by a bias; and
a control circuit comprising a first input for receiving a first control indicator,
the control circuit being capable of outputting the bias at different levels
according to the first control indicator.
 - [c2] 2. The differential signal transmitter of claim 1 wherein the first control indicator is a single bit.
 - [c3] 3. The differential signal transmitter of claim 1 wherein the control circuit is capable of outputting the bias at different current levels.
 - [c4] 4. The differential signal transmitter of claim 3 wherein when the control circuit outputs the bias at a first current level, the driver circuit generates a Low Voltage Differential Signaling (LVDS) differential signal in response to the inputted data.
 - [c5] 5. The differential signal transmitter of claim 3 wherein when the control circuit outputs the first current level as a bias for the driver circuit, the driver circuit generates a Mini-LVDS differential signal in response to inputted data.
 - [c6] 6. The differential signal transmitter of claim 3 wherein when the control circuit outputs the first current level as a bias for the driver circuit, the driver circuit generates a Reduced Swing Differential Signaling differential signal in response to inputted data.
 - [c7] 7. The differential signal transmitter of claim 1 wherein when the control circuit outputs a predetermined current level as a bias for the driver circuit, the outputted current is a sum of predetermined current sources, the current sources to summed being selected according to the control indicator.
 - [c8] 8. The differential signal transmitter of claim 1 wherein the control circuit further comprises a second input for receiving a second control indicator.

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